

**isc N-Channel MOSFET Transistor**
**IXFH15N100Q3**
**DESCRIPTION**

- Drain Current :  $I_D = 15A @ T_C = 25^\circ C$
- Drain Source Voltage :  $V_{DSS} = 1000V(\text{Min})$
- Static Drain-Source On-Resistance :  $R_{DS(on)} = 1.05 \Omega (\text{Max}) @ V_{GS} = 10V$
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**APPLICATIONS**

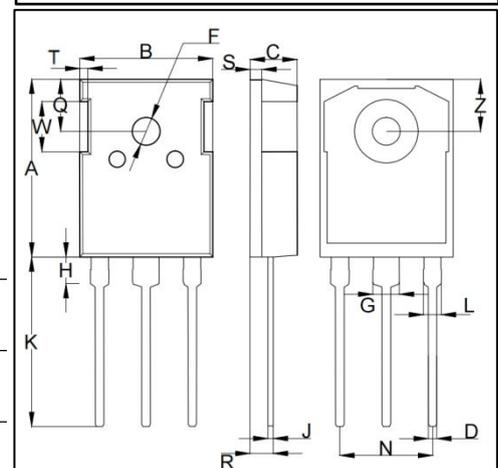
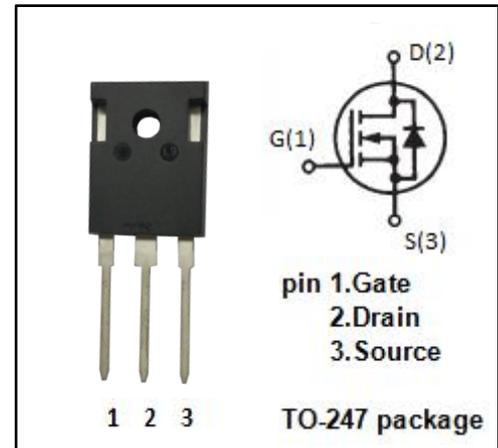
- motor drive, DC-DC converter, power switch and solenoid drive.

**ABSOLUTE MAXIMUM RATINGS( $T_a = 25^\circ C$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage ( $V_{GS}=0$ )	1000	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D$	Drain Current-continuous@ $T_C = 25^\circ C$	15	A
$I_{DM}$	Drain Current-Single Pulse	45	A
$P_{tot}$	Total Dissipation@ $T_C = 25^\circ C$	600	W
$T_j$	Max. Operating Junction Temperature	-55~150	$^\circ C$
$T_{stg}$	Storage Temperature Range	-55~150	$^\circ C$

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
$R_{th j-c}$	Thermal Resistance, Junction to Case	0.21	$^\circ C/W$



DIM	mm	
	MIN	MAX
A	19.80	21.50
B	15.40	15.90
C	4.70	5.30
D	0.90	1.26
F	3.50	3.90
G	2.70	3.30
H	3.90	4.10
J	0.500	0.700
K	19.50	20.50
L	1.90	2.20
N	10.80	11.00
Q	6.00	6.30
R	2.90	3.30
S	1.80	2.20
T	2.15	2.35
W	4.90	5.10
Z	6.00	6.30

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**ELECTRICAL CHARACTERISTICS (T<sub>C</sub>=25°C)**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0; I <sub>D</sub> = 0.25mA	1000	--	--	V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> ; I <sub>D</sub> = 0.25mA	2.5	--	4.5	V
R <sub>DS(on)</sub>	Drain-Source On-stage Resistance	V <sub>GS</sub> = 10V; I <sub>D</sub> = 7.5A	--	--	1.05	Ω
I <sub>GSS</sub>	Gate Source Leakage Current	V <sub>GS</sub> = ±30V; V <sub>DS</sub> = 0	--	--	±0.1	uA
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 1000V; V <sub>GS</sub> = 0	--	--	25	uA
V <sub>SD</sub>	Forward On-Voltage	I <sub>S</sub> = 15A; V <sub>GS</sub> = 0	--	--	1.4	V
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1.0MHz	--	7600	--	pF
C <sub>oss</sub>	Output Capacitance		--	760	--	
C <sub>rss</sub>	Reverse Transfer Capacitance		--	88	--	
Q <sub>g</sub>	Total Gate Charge	V <sub>DD</sub> = 500V, I <sub>D</sub> = 12A, V <sub>GS</sub> = 10V	--	193	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	37	--	
Q <sub>gd</sub>	Gate-Drain Charge		--	70	--	
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> = 500V, I <sub>D</sub> = 12A, V <sub>GS</sub> = 10V, R <sub>G</sub> = 25Ω	--	66	--	ns
t <sub>r</sub>	Turn-on Rise Time		--	48	--	
t <sub>d(off)</sub>	Turn-off Delay Time		--	80	--	
t <sub>f</sub>	Turn-off Fall Time		--	50	--	
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0V, I <sub>S</sub> = 24A, di <sub>F</sub> /dt = 100A /μs	--	550	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge		--	5.0	-	uC

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